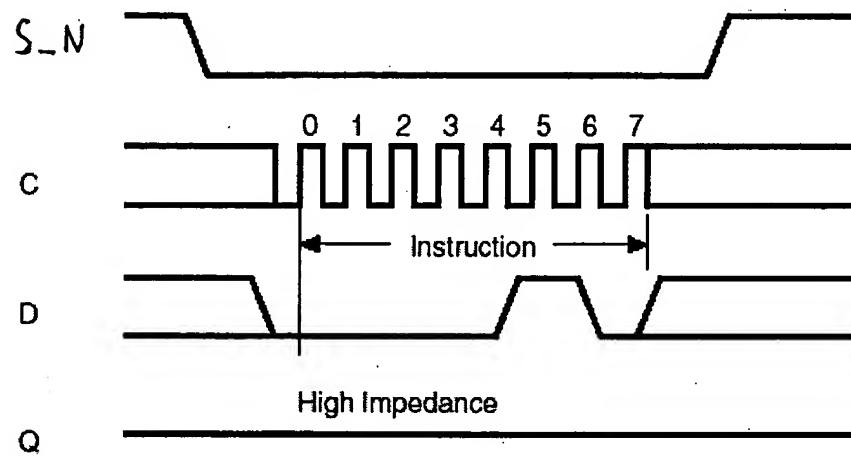
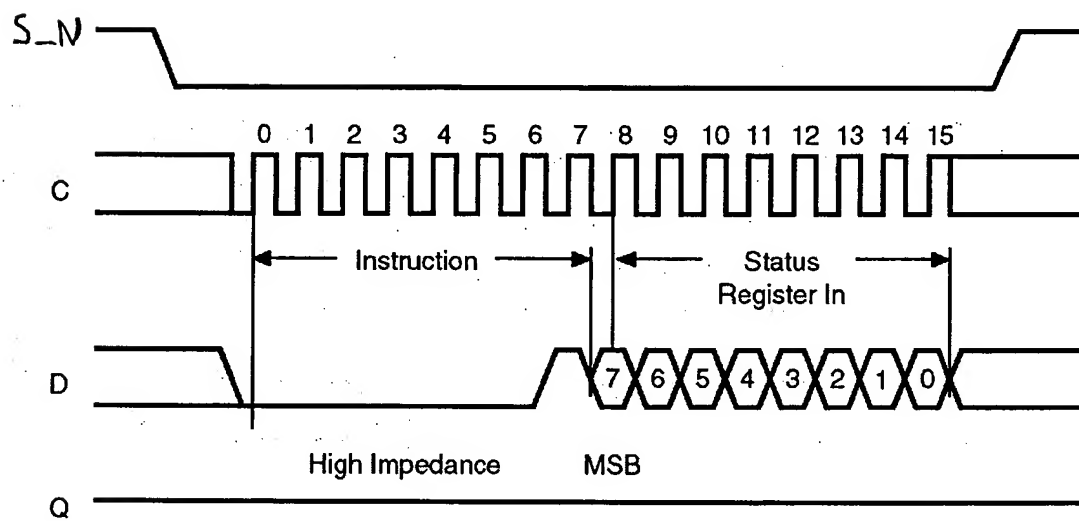


Title: FAST PAGE PROGRAMMING ARCHITECTURE AND METHOD IN A NON-VOLATILE  
MEMORY DEVICE WITH AN SPI INTERFACE

Inventor(s): Paolino Schillaci et al. Docket No.: 856063.759 Express Mail No. EV336599814US



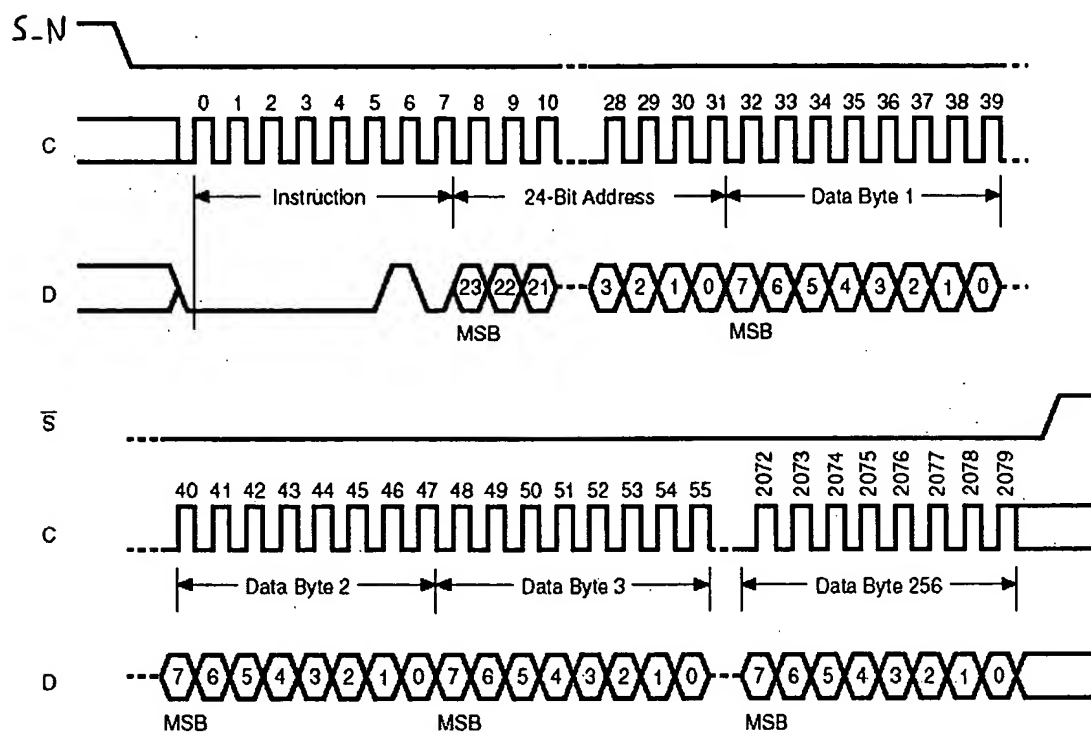
**Fig.1**



**Fig.2**

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**Fig.3**

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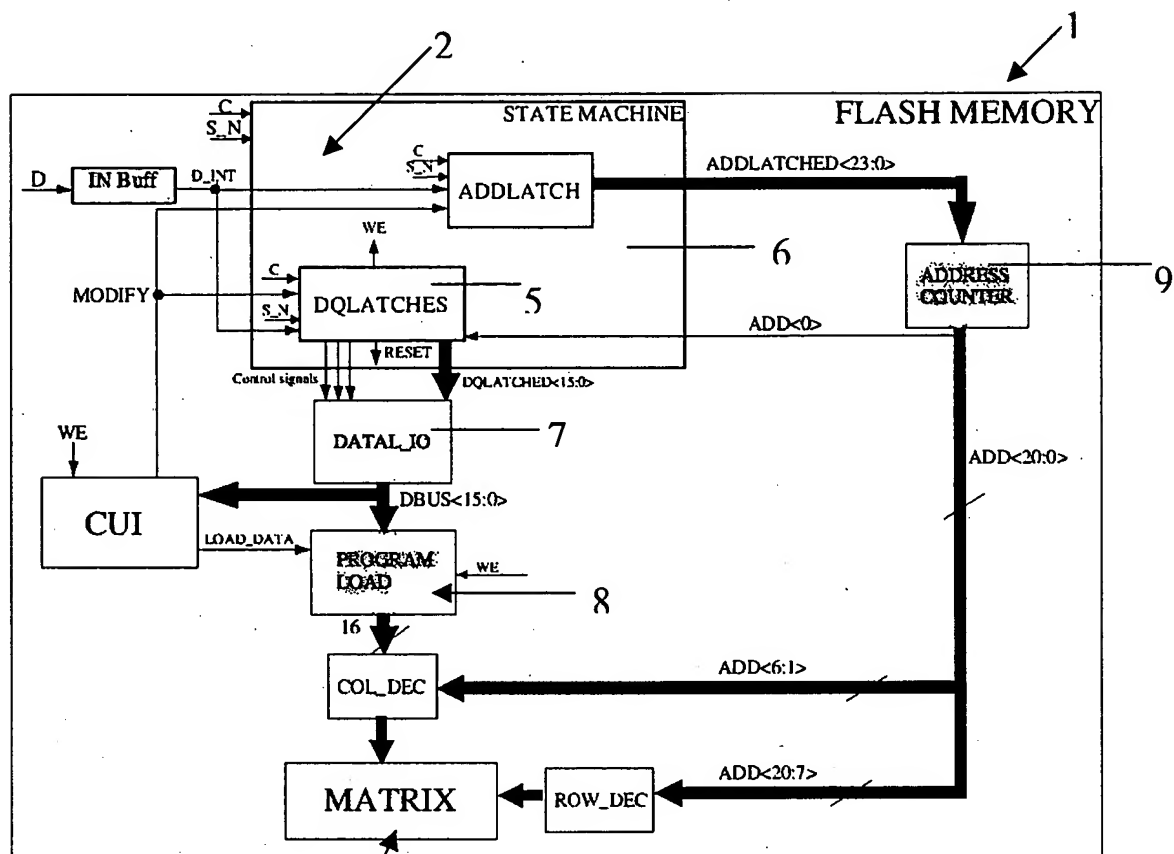


Fig.4

3

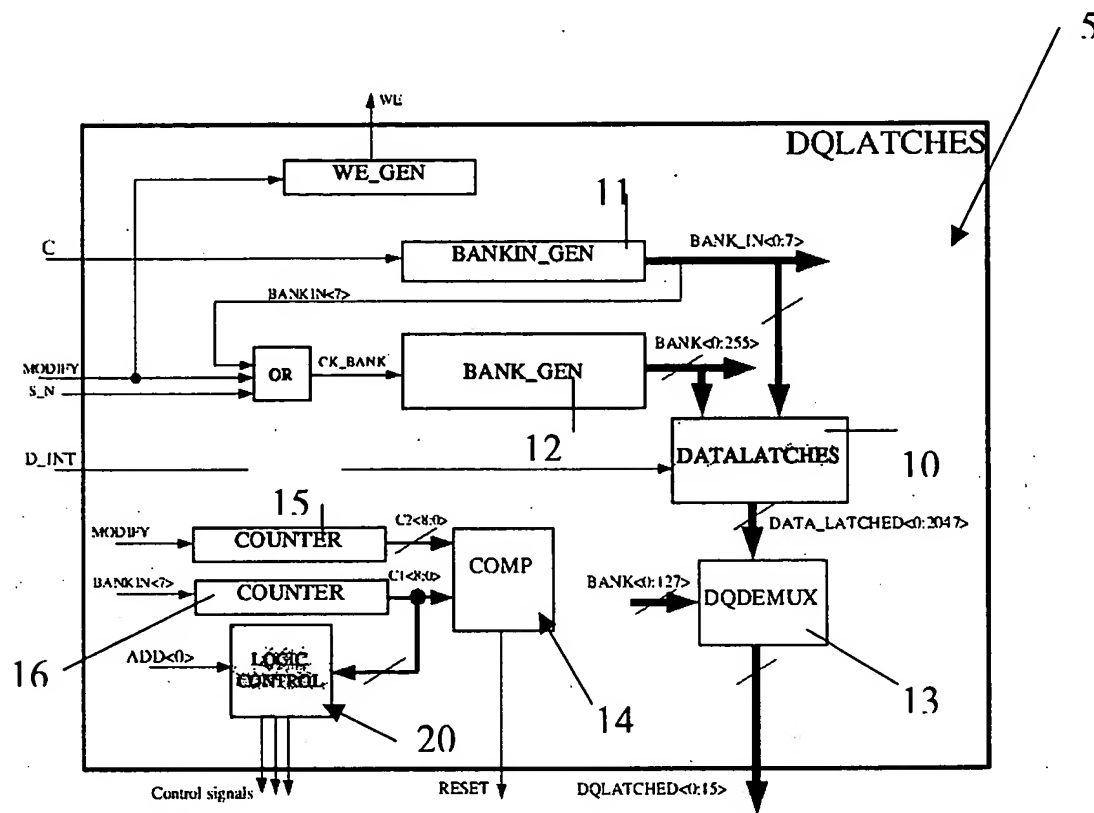


Fig.5

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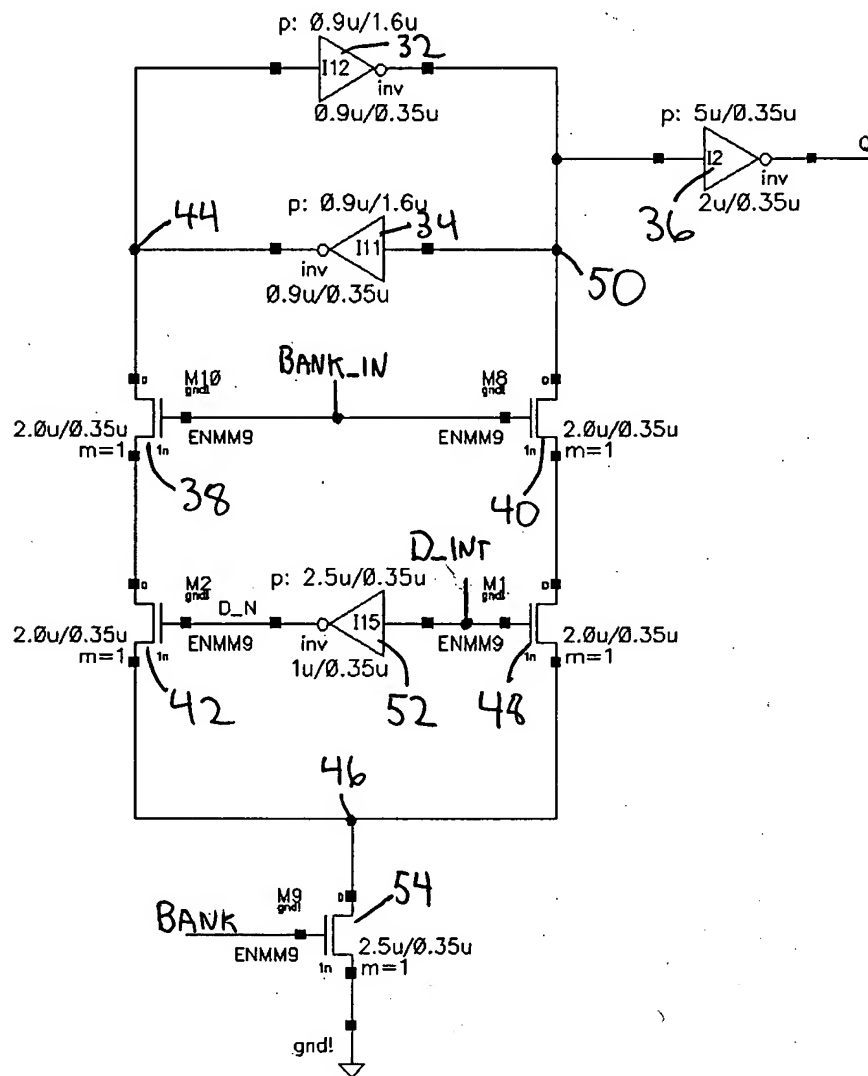


Fig.6

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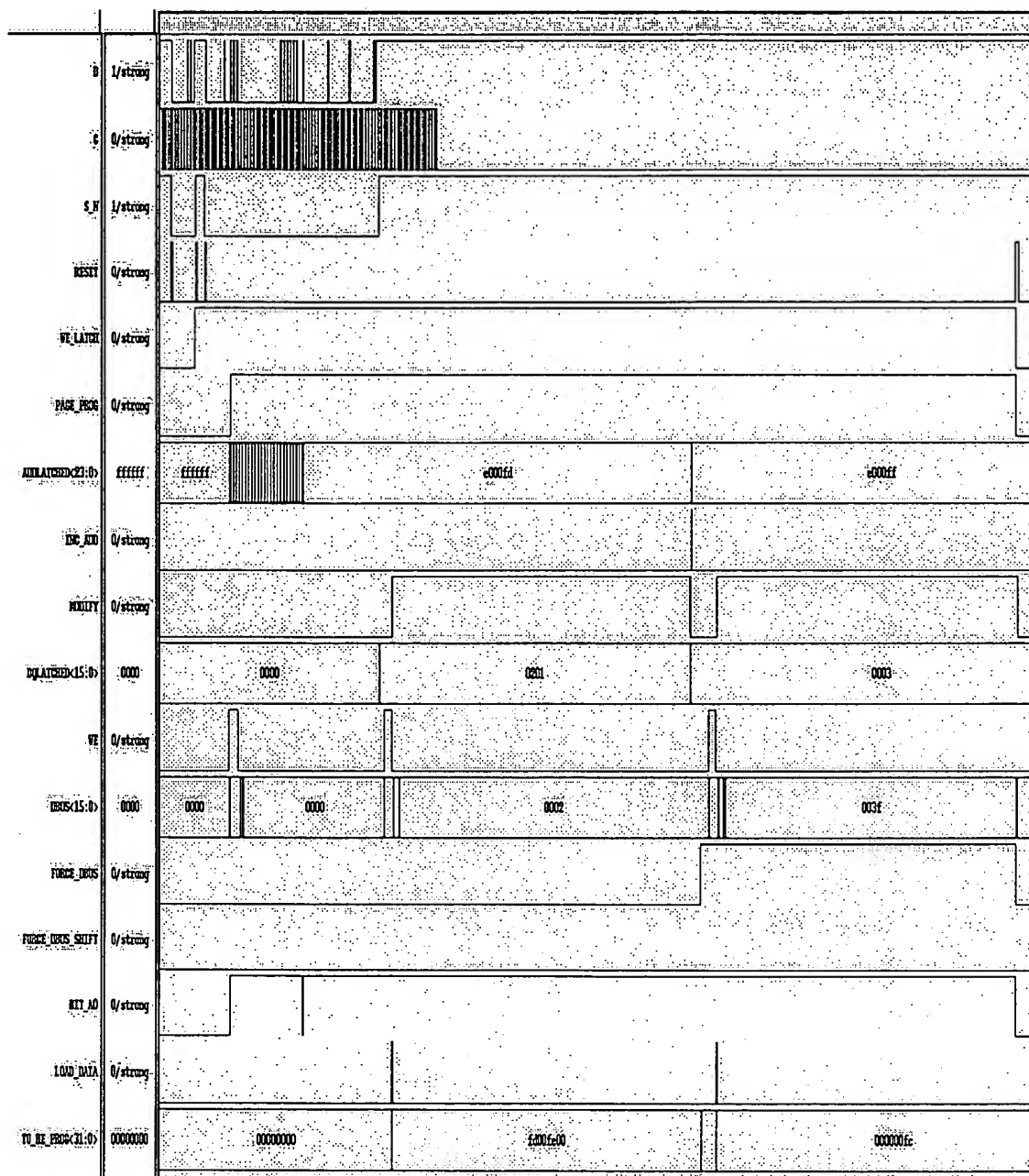


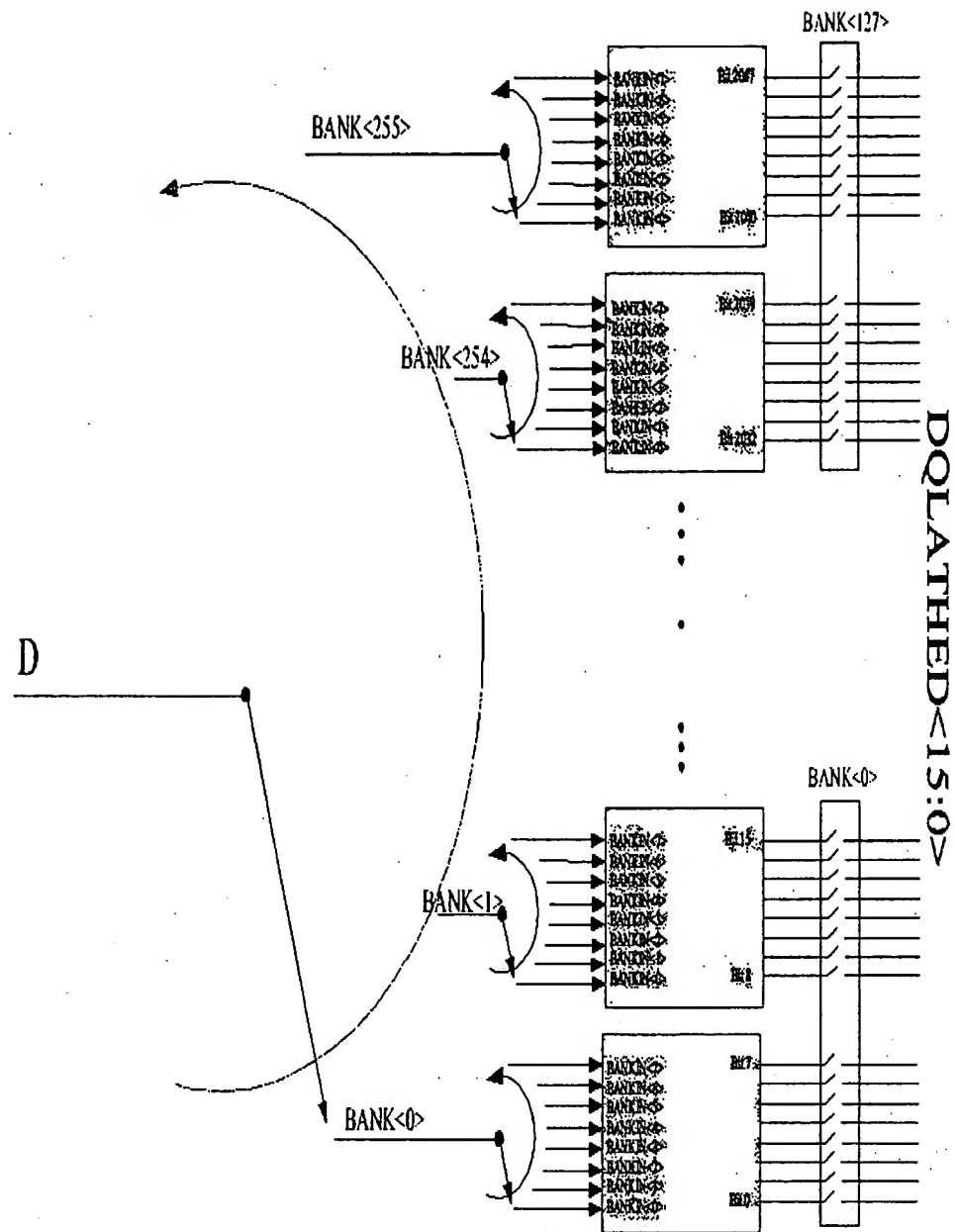
Fig.7

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**Fig.9**